



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/757,276
Filing Date January 14, 2004
Inventor Kevin L. Beaman et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Stephen W. Smoot
Attorney's Docket No. MI22-2475
Customer No. 021567
Title: Semiconductor Assemblies, Methods of Forming Structures over Semiconductor
Substrates, and Methods of Forming Transistors Associated with Semiconductor
Substrates

RESPONSE TO FEBRUARY 6, 2006 FINAL OFFICE ACTION
ACCOMPANYING REQUEST FOR CONTINUED EXAMINATION (RCE)

*Entered
With
RCE
6/2/06*

To: Mail Stop: RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: D. Brent Kenady
Tel. 509-624-4276; Fax 509-838-3424
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3817

Responsive to the Final Office Action dated February 6, 2006, Applicant amends
and remarks as follows:

AMENDMENTS

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

EV850820420